In the claims:

Following is a complete set of claims as amended with this Response.

1. (Currently Amended) A method comprising:

determining a start of reception of radio signals by a radio ~~based on predicting a start time of a time slot of a repeating TDMA frame assigned to the radio~~;

generating a radio active packet, the packet including a radio receive ~~signal based on the predicted time slot~~ start time and the packet being addressed to a component of a coupled computer;

transmitting the radio active packet to the ~~signal to a~~ coupled computer over a high speed data communications bus to affect the radio interference generated by the coupled computer~~;~~

~~determining an end of reception of radio signals by the radio based on predicting an end time of the time slot assigned to the radio;~~

~~generating a radio not active signal based on the predicted time slot end time; and~~

~~transmitting the radio not active signal to the coupled computer to affect the radio interference generated by the coupled computer~~.

2. (Canceled)

3. (Currently Amended) The method of Claim 1, wherein determining the start of reception ~~predicting the start time~~ comprises predicting the start time using a timing reference of the radio.

4. (Currently Amended) The method of Claim 1, wherein a time slot of a repeating time division multiplex frame has been assigned to the radio, and wherein the assigned time slot is a receive time slot assigned to the coupled computer and wherein

predicting the start time comprises predicting the start time using the coupled computer's clock as a timing reference.

5. (Currently Amended) The method of Claim 1, wherein transmitting the radio active packet comprises setting an interrupt in a status register of the high speed data communications bus. ~~signal comprises asserting a state on a connector between the radio and the coupled computer.~~

6. (Currently Amended) The method of Claim 5, wherein transmitting the radio active packet comprises loading the packet in a register that is linked to servicing the interrupt. ~~not active signal comprises de-asserting the state on the connector between the radio and the coupled computer.~~

7. (Currently Amended) The method of Claim 1, wherein the radio active packet includes a radio receive start time and a radio receive end time in a single packet. ~~transmitting the radio active signal comprises sending an instruction over a high speed system bus to the coupled computer.~~

8. (Currently Amended) The method of Claim 1 ~~Claim 7~~, wherein transmitting the radio active packet comprises addressing the packet ~~sending an instruction comprises sending an interrupt signal~~ to CPU operating software of the coupled computer.

9. (Currently Amended) The method of Claim 1 ~~Claim 7~~, wherein transmitting the radio active packet comprises addressing the packet ~~sending an instruction comprises sending an instruction~~ to a power management module of the coupled computer.

10. (Currently Amended) The method of Claim 1, wherein transmitting the radio active packet comprises addressing the packet to a disk drive ~~not active signal comprises sending a hardware interrupt to wake the CPU~~ of the coupled computer.

11. (Currently Amended) The method of Claim 1, wherein the radio active packet indicates ~~signal and the radio not active signal comprise a single signal indicating~~ the start time and the duration of the radio reception.

12. (Currently Amended) The method of Claim 7, further comprising ~~Claim 1, wherein determining the end of reception comprises~~ predicting the end of reception based on the start time and the expected duration of reception.

13. (Currently Amended) A machine-readable medium having stored thereon data representing instructions which, when executed by a machine, cause the machine to perform operations comprising:

determining a start of reception of radio signals by a radio ~~based on predicting a start time of a time slot of a repeating TDMA frame assigned to the radio~~;

generating a radio active packet, the packet including a radio receive ~~signal based on the predicted time slot~~ start time and the packet being addressed to a component of a coupled computer;

transmitting the radio active packet to the ~~signal to a~~ coupled computer over a high speed data communications bus to affect the radio interference generated by the coupled computer;

~~determining an end of reception of radio signals by the radio based on predicting an end time of the time slot assigned to the radio;~~

~~generating a radio not active signal based on the predicted time slot end time; and~~

~~transmitting the radio not active signal to the coupled computer to affect the radio interference generated by the coupled computer.~~

14. (Canceled)

15. (Currently Amended) The medium of Claim 13, wherein the instructions for determining the start of reception ~~predicting the start time~~ comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising predicting the start time using a timing reference of the radio.

16. (Currently Amended) The medium of Claim 13, wherein the radio active packet includes a radio receive start time and a radio receive end time in a single packet. ~~instructions for transmitting the radio active signal comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising sending an instruction over a high speed system bus to the coupled computer.~~

17. (Currently Amended) The medium of Claim 16, wherein the instructions for transmitting a radio active packet ~~sending an instruction~~ comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising ~~sending an instruction~~ addressing the packet to a power management module of the coupled computer.

18. (Currently Amended) The medium of Claim 13, wherein the radio active packet indicates ~~signal and the radio not active signal comprise a single signal indicating~~ the start time and the duration of the radio reception.

19. (Currently Amended) The medium of Claim 13, wherein the instructions further ~~for determining the end of reception~~ comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising predicting the end of reception based on the start time and the expected duration of reception.

20. (Currently Amended) A radio comprising:

a receiver;

a processor to determine a start of reception of radio signals by the receiver ~~based on predicting a start time of a time slot of a repeating TDMA frame assigned to the radio~~ and generate a radio active packet including the radio reception ~~signal based on the predicted time slot~~ start time and the packet being addressed to a component of a coupled computer ~~to determine an end of reception of radio signals by the receiver based on predicting an end time of the time slot assigned to the radio and generate a radio not active signal based on the predicted time slot end time~~; and

an external interface to a high speed data communications bus of the coupled computer to transmit the radio active packet over the bus to the ~~signal and the radio not active signal to a~~ coupled computer to affect the radio interference generated by the coupled computer.

21. (Original) The radio of Claim 20, further comprising a timing reference coupled to the processor for use in determining the start of reception and the end of reception by prediction.

22. (Currently Amended) The radio of Claim 20, further comprising a connector between the radio and the coupled computer coupled to the external interface and wherein the processor sets an interrupt in a status register of the high speed data communication bus through ~~external interface transmits the radio active signal by asserting a state on~~ the connector.

23. (Currently Amended) The radio of Claim 22, wherein the external interface transmits the radio active packet byloading the packet in a register that is linked to servicing the interrupt ~~not active signal by de-asserting the state on the connector~~.

24. (Currently Amended) The radio of Claim 20, wherein the radio active packet is addressed ~~signal comprises an interrupt signal~~ to CPU operating software of the coupled computer.

25. (Currently Amended) The radio of Claim 20, wherein the radio active packet is addressed ~~signal comprises an instruction~~ to a power management module of the coupled computer.

26. (Currently Amended) The radio of Claim 20, wherein the radio active packet indicates ~~signal and the radio not active signal comprise a single signal indicating~~ the start time and the duration of the radio reception.

27. (Currently Amended) A method comprising:

receiving a radio active packet over a high speed data communications bus ~~signal~~ at a computer having a CPU from a coupled radio, the radio active packet ~~signal~~ indicating a start of reception of radio signals by the coupled radio and being addressed to a component of the computer ~~based on predicting a start time of a time slot of a repeating TDMA frame assigned to the radio~~; and

adjusting system operating parameters of the computer in response to the packet to reduce interference with the radio.~~;~~

~~receiving a radio not active signal at the computer from the coupled radio, the radio not active signal indicating an end of reception of radio signals by the radio based on predicting an end time of the time slot assigned to the radio; and~~

~~readjusting the system operating parameters of the computer for operation without regard to interference with the radio.~~

28. (Currently Amended) The method of Claim 27, wherein receiving the radio active packet includes receiving an interrupt in a status register of the high speed

data communications bus, the method further comprising polling the register for the interrupt ~~coupled radio for a radio active signal before receiving the radio active signal~~.

29. (Canceled)

30. (Currently Amended) The method of Claim 27, wherein a time slot of a repeating time division multiplex frame has been assigned to the coupled radio, and wherein the assigned time slot is a receive time slot and wherein predicting the start time comprises predicting the start time using the computer's clock as a timing reference.

31. (Currently Amended) The method of Claim 27, wherein receiving the radio active packet ~~signal~~ comprises receiving an interrupt in a status register of the high speed data communications bus of ~~detecting the assertion of a state on a connector between the radio and~~ the computer.

32. (Currently Amended) The method of Claim 31, wherein receiving the radio active packet ~~not active signal~~ comprises loading the packet in a register that is linked to servicing the interrupt ~~detecting the de-assertion of the state on the connector between the radio and the computer~~.

33. (Currently Amended) The method of Claim 27, wherein the radio active packet includes a radio receive start time and a radio receive end time in a single packet. ~~receiving the radio active signal comprises receiving an instruction over a communications bus coupled to the coupled radio.~~

34. (Currently Amended) The method of Claim 33, wherein receiving the radio active packet ~~an instruction~~ comprises receiving the packet addressed ~~an interrupt signal~~ to CPU operating software of the computer.

35. (Currently Amended) The method of Claim 33, wherein receiving the radio active signal ~~an instruction~~ comprises receiving the packet addressed ~~an instruction~~ to a power management module of the computer.

36. (Currently Amended) The method of Claim 27, wherein receiving the radio active packet ~~not active signal~~ comprises receiving the packet addressed to a disk drive ~~a hardware interrupt to wake the CPU of the computer~~.

37. (Currently Amended) The method of Claim 27, wherein the radio active packet comprises ~~signal and the radio not active signal comprise~~ a single packet ~~signal~~ indicating the start time and the duration of the radio reception.

38. (Original) The method of Claim 27, wherein adjusting the system operating parameters comprises reducing the system clock rate.

39. (Original) The method of Claim 27, wherein adjusting the system operating parameters comprises turning off a CPU clock of the computer.

40. (Original) The method of Claim 27, wherein adjusting the system operating parameters comprises interrupting traffic on the computer system bus.

41. (Original) The method of Claim 27, wherein adjusting the system operating parameters comprises suspending operation of selected peripheral components of the computer.

42. (Currently Amended) A machine-readable medium having stored thereon data representing instructions which, when executed by a machine, cause the machine to perform operations comprising:

receiving a radio active packet over a high speed data communications bus ~~signal~~ at a computer having a CPU from a coupled radio, the radio active packet ~~signal~~ indicating a start of reception of radio signals by the coupled radio and being addressed to

a component of the computer ~~based on predicting a start time of a time slot of a repeating TDMA frame assigned to the radio~~; and

adjusting system operating parameters of the computer in response to the packet to reduce interference with the radio.~~;~~

~~receiving a radio not active signal at the computer from the coupled radio, the radio not active signal indicating an end of reception of radio signals by the radio based on predicting an end time of the time slot assigned to the radio; and~~

~~readjusting the system operating parameters of the computer for operation without regard to interference with the radio.~~

43. (Currently Amended) The medium of Claim 42, further comprising instructions which, when executed by the machine, cause the machine to perform further operations comprising polling a status register on the high speed data communications bus for an interrupt and then reading the packet from an associated register ~~the coupled radio for a radio active signal before receiving the radio active signal~~.

44. (Canceled)

45. (Currently Amended) The medium of Claim 43 ~~Claim 42~~, wherein the instructions for receiving the radio active packet ~~signal~~ comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising loading the packet in a register that is linked to servicing the interrupt ~~detecting the assertion of a state on a connector between the radio and the computer~~.

46. (Currently Amended) The medium of Claim 42, wherein the radio active packet includes a radio receive start time and a radio receive end time in a single packet. ~~instructions for receiving the radio active signal comprise instructions which, when~~

~~executed by the machine, cause the machine to perform further operations comprising receiving an instruction over a communications bus coupled to the coupled radio.~~

47. (Currently Amended) The medium of Claim 46, wherein the instructions for receiving the radio active packet ~~an instruction~~ comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising receiving the packet addressed ~~an interrupt signal~~ to CPU operating software of the computer.

48. (Original) The medium of Claim 42, wherein the instructions for adjusting the system operating parameters comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising reducing the system clock rate.

49. (Original) The medium of Claim 42, wherein the instructions for adjusting the system operating parameters comprise instructions which, when executed by the machine, cause the machine to perform further operations comprising turning off a CPU clock of the computer.

50. (Currently Amended) A computer comprising:

an I/O data communications bus to receive a radio active packet ~~signal and a radio not active signal~~ from a coupled radio, the radio active packet ~~signal~~ indicating a start time and an end time for ~~of~~ reception of radio signals by the coupled radio, the packet being addressed to a component of the computer ~~based on predicting a start time of a time slot of a repeating TDMA frame assigned to the radio and the radio not active signal indicating an end of reception of radio signals by the radio based on predicting an end time of the time slot assigned to the radio~~; and

a CPU coupled to the bus to adjust system operating parameters of the computer to reduce interference with the ~~radio in response to the radio active signal~~ and to readjust the system operating parameters of the computer for operation without regard to interference with the radio in response to the radio active packet ~~not active signal~~.

51. (Currently Amended) The computer of Claim 50, further comprising a timing reference clock to use in predicting a start time for reception by the radio in response to the radio active packet ~~signal~~.

52. (Currently Amended) The computer of Claim 50, further comprising a connector coupled to the I/O data communications bus and wherein the I/O data communications bus receives the radio active packet ~~signal~~ by detecting ~~the assertion of~~ a state of a status register on the connector and reading the radio active packet from an associated register.

53. (Currently Amended) The computer of Claim 52, wherein the I/O data communications bus receives the radio active packet ~~not active signal~~ by receiving an interrupt in a status register of the I/O data communications bus and the radio active packet in a register linked to the status register ~~detecting the de-assertion of the state on the connector~~.

54. (Currently Amended) The computer of Claim 50, wherein the radio active packet is addressed ~~signal comprises an interrupt signal~~ to the CPU.

55. (Original) The computer of Claim 54, further comprising a power management module coupled to the CPU to receive an instruction from the CPU to execute power management functions to reduce interference.

56. (Currently Amended) The computer of Claim 50, further comprising a power management system and wherein the radio active packet is addressed to a power management system ~~not active signal comprises a hardware interrupt to wake the CPU~~.

57. (Currently Amended) The computer of Claim 50, wherein the radio active packet comprises ~~signal and the radio not active signal comprise~~ a single packet ~~signal~~ indicating the start time and the duration of the radio reception.

58. (Original) The computer of Claim 50, wherein interference is reduced by reducing the system clock rate.

59. (Original) The computer of Claim 50, wherein interference is reduced by turning off a CPU clock of the computer.

60. (Original) The computer of Claim 50, wherein interference is reduced by interrupting traffic on the computer system bus.

61. (Original) The computer of Claim 50, wherein interference is reduced by suspending operation of selected peripheral components of the computer.

62. (New) A method comprising:

determining a start of reception of radio signals by a radio ;

generating a radio active packet, the packet including a radio receive start time and the packet being addressed to a component of a coupled computer;

transmitting the radio active packet to the component of the coupled computer to which the packet is addressed to affect the radio interference generated by the coupled computer.

63. (New) The method of Claim 62, wherein a time slot of a repeating time division multiplex frame has been assigned to the radio, and wherein the assigned time

slot is a receive time slot assigned to the coupled computer and wherein predicting the start time comprises predicting the start time of the assigned time slot.

64. (New) The method of Claim 62, wherein transmitting the radio active packet comprises setting an interrupt in a status register of the high speed data communications bus.

65. (New) The method of Claim 64, wherein transmitting the radio active packet comprises loading the packet in a register corresponding to the component to which the packet is addressed.

66. (New) The method of Claim 62 , wherein the component to which the packet is addressed corresponds to CPU operating software of the coupled computer.

67. (New) The method of Claim 62, wherein the component to which the packet is addressed comprises a power management module of the coupled computer.